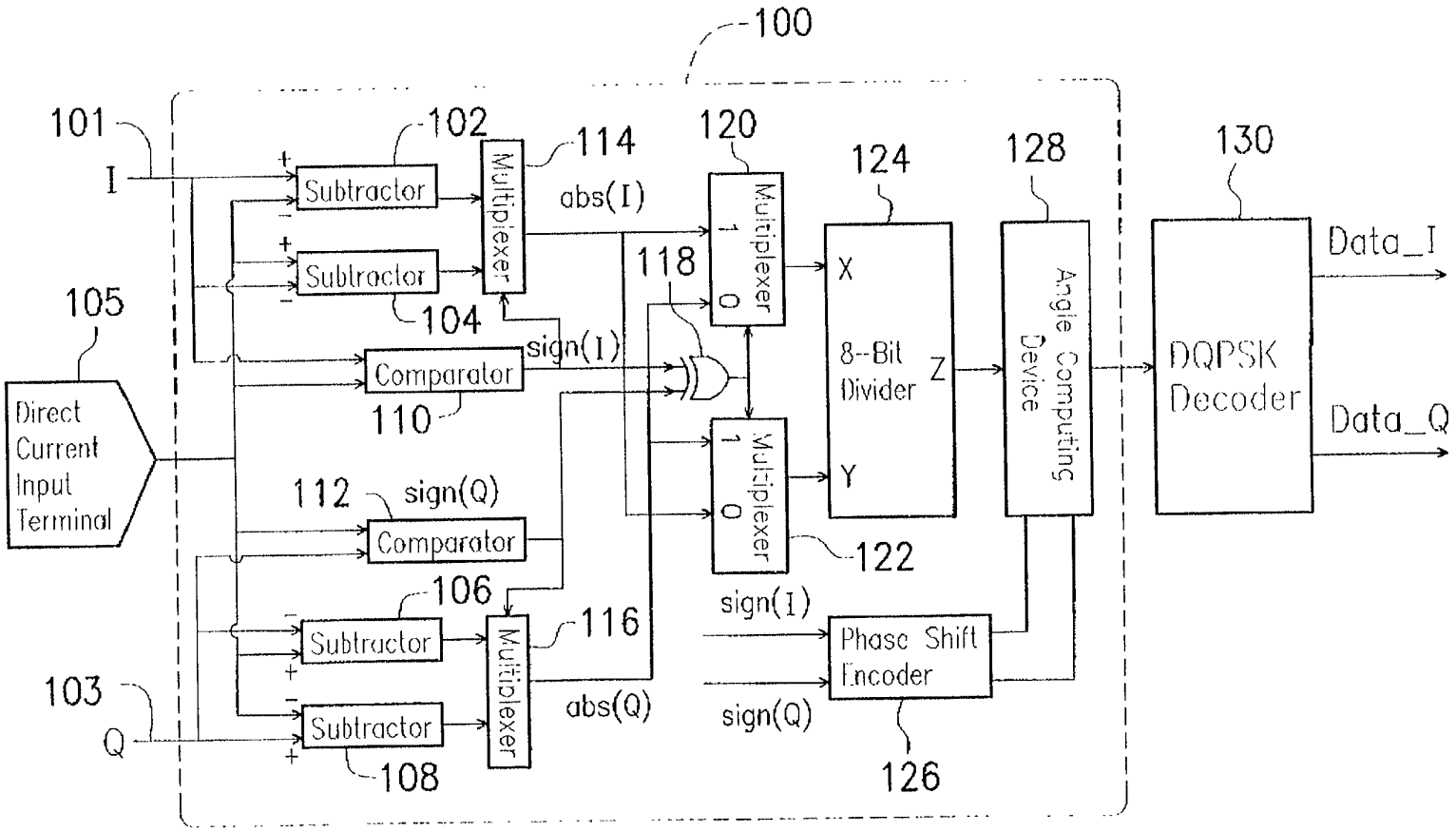
100
101
I
102
Subtractor
Subtractor
104
Multiplexer
114
abs(I)
120
Multiplexer
1
0
124
X
8--Bit
Divider
Y
Z
128
Angle Computing Device
130
DQPSK Decoder
Data_I
Data_Q
105
Direct Current Input Terminal
Comparator
110
sign(I)
118
112
sign(Q)
Comparator
Multiplexer
1
0
122
106
Subtractor
Subtractor
108
Multiplexer
116
abs(Q)
sign(I)
sign(Q)
Phase Shift Encoder
126
103
Q

FIG. 1